

Department of Electronics and Communication Engineering

Personal Details

Name:	Dr.T.Saravanan
Designation:	Professor & Principal
Educational Qualification:	M.E.,Ph.D.
Experience:	19 years
Area of Specialization:	Embedded System

Educational Details

S.No.	Degree	Branch/Specialization	Institute/University	Year
1	B.E	Electronics and Communication	Madras University	2002
2	M.E.	Process control and Instrumentation	Annamalai University	2005
3	Ph.D.	Embedded System	Sathyabama University	2012

Publication Details

International Journals

- 1) Technical paper titled "CLOSED LOOP CLL TYPE AC/DC CONVERTER USING ORCADE"
Published in "Journal June 2007(ISSN 0912-3251)
- 2) Technical paper titled "SIMULATION AND MODELING OF CLL TYPE AC/DC CONVERTER"
Published in "International Journal of Realization of Electrical Apparatus" Journal July 2010.(ISSN 0974-5815,Jan-Jun2010)
- 3) Technical paper titled "MODELLING AND SIMULATION OF CLOSED LOOP CONTROLLED MULTIPLE OUTPUT AC / DC CONVERTER" Published in "International Journal of Engineering Research and Industrial Application" Journal Nov 2010. . (ISSN 0974-1518, Feb2011).

National Journals

- 4) Technical paper titled "SIMULATION OF MULTIPLE OUTPUT AC/DC CONVERTERS" Published in "i-Manager's National Journal of Journal on Electrical Engineering" .(ISSN 0973-8835,Oct-Dec

2010).

- 5) Technical paper titled "HARMONIC ANALYSIS FOR CLL TYPE CONVERTER WITH MULTI OUTPUT", Published in "International Journal of Technology Today", Vol. 3, No. 2, pp. 155-162,
- 6) Technical paper titled "PERFORMANCE ANALYSIS OF FTTH USING GEAPON IN DIRECT AND EXTERNAL MODULATION", "Indian Journal of Science and Technology", Vol-6,Supplementary-6, June-2013. (Scopus Index).
- 7) Technical paper titled "RECOVERY OF LINK QUALITY DEGRADATION IN WIRELESS MESH NETWORKS", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, and June-13. (Scopus Index).
- 8) Technical paper titled "RECOVERY OF LINK QUALITY DEGRADATION IN WIRELESS MESH NETWORKS", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, and June-13. (Scopus Index).
- 9) Technical paper titled "A TWO STAGE DC-DC CONVERTER WITH ISOLATION FOR RENEWABLE ENERGY APPLICATIONS", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, June-13. (Scopus Index).
- 10) Technical paper titled "ANALYSIS OF POLARIZATION MODE DISPERSION IN FIBERS AND ITS MITIGATION USING AN OPTICAL COMPENSATION TECHNIQUE", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, June-13.
- 11) Technical paper titled "CHROMATIC DISPERSION COMPENSATION IN OPTICAL FIBER COMMUNICATION SYSTEM AND ITS SIMULATION", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, June-13. (Scopus Index).
- 12) Technical paper titled "SYNTHESIS AND STRUCTURAL CHARACTERIZATION OF THIN FILMS OF SNO₂ PREPARED BY SPRAY PYROLYSIS TECHNIQUE", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, June-13. (Scopus Index).
- 13) Technical paper titled "PERFORMANCE ANALYSIS OF A TWO CHANNEL OPTICAL WDM SYSTEM USING BINARY AND DUO BINARY MODULATION FORMATS", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, May-13. (Scopus Index).
- 14) Technical paper titled "BUCK CONVERTER WITH A VARIABLE NUMBER OF PREDICTIVE CURRENT DISTRIBUTING METHOD", "Indian Journal of Science and Technology", Vol-6, Supplementary-6, May-13. (Scopus Index).

Professional Society Membership

1. ISTE
2. CSI
3. IETE
4. IEI
5. IEEE

Program Attended

FDP/STTP

- Participate in one day seminar on “A REFRESHER COURSE ON VLSI-TECHNOLOGIES” at Sakthi Mariamman Engineering College, Chennai On 03-09-2005.
- Participate short term training programme on “Recent trends in process control and instrumentation” conducted at SRM engineering College on 22-11-2005 to 24-11-2005.
- Participate in the three day workshop on “PLC and SCADA” organized by Easwari Engineering College, Ramapuram, Chennai-89 from 09-02-2006 to 11-02-2006 in association with M/s Siemens India Ltd.
- Participate in the workshop “PLC, PAC & DRIVES” on 23-05-2007 in Valliammai Engineering College, Chennai.
- Attended the Faculty Development Programme on “ADVANCED PROCESS CONTROL & VLSI DESIGN” on 03-04-2008. Valliammai Engineering College, Chennai.